

L Number	Hits	Search Text	DB	Time stamp
40	38	master\$1 with slave\$1 with control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:19
41	34	(master\$1 with slave\$1 with control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @pt1d<20010501 or @pd<20010501)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:21
42	7027	control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:26
43	1790	(control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:20
44	16	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)) same master\$1 same slave\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:20
45	13	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)) same master\$1 same slave\$1 and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @pt1d<20010501 or @pd<20010501)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:27
46	8	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)) same master\$1 same slave\$1 and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @pt1d<20010501 or @pd<20010501)) not (master\$1 with slave\$1 with control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:21
47	713	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:30
48	15	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with instead	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:27

49	13	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with instead) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @ptld<20010501 or @pd<20010501)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:31
50	2370	(control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:30
51	194	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with (instead or replac\$7 or plac\$7)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:42
52	127	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with instead	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:31
53	115	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with instead) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @ptld<20010501 or @pd<20010501)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:42
54	102	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with instead) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @ptld<20010501 or @pd<20010501)) not (((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with instead) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @ptld<20010501 or @pd<20010501))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:42
55	168	((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with (instead or replac\$7 or substitu\$7)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:42

56	155	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with (instead or replac\$7 or substitu\$7)) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @pt1d<20010501 or @pd<20010501)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:42
57	40	(((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with (instead or replac\$7 or substitu\$7)) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @pt1d<20010501 or @pd<20010501)) not ((((((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (implement\$5 or compris\$5)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with instead) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @pt1d<20010501 or @pd<20010501)) or (((control\$5 with (fpga\$1 or pld\$1 or pga\$1 or epld\$1)) with (cpu\$1 or microprocess\$3 or mpu\$1 or dsp\$1 or process?r\$1 or (processing adj2 (unit\$1 or device\$1 or agent\$1 or node\$1 or module\$1)))) with instead) and (@ad<20010501 or @rlad<20010501 or @prad<20010501 or @pt1d<20010501 or @pd<20010501)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/27 13:43